START OBTAIN ASSEMBLY LANGUAGE PROBRAM DETERMINE STATIC FREGUENCY EACH DISTRUCTION JUSTRUCTION 106 FREQUENCY DETERMINE NO. of TYPE OF INSTRUCTIONS NOTESSANG FOR Consect Magram ESECUTION CREATE Congressed 500 INSTRUCTION ENCODING RE-EVALUATE Compressed INSTRUCTION SOT GENERATE NEW ENODNG FOR compressed INSTRUCTION SOT م ن مری

F.6.1

100

31 30 29 28 27		14 13	0
Openic	Instruction 2	In	nstruction 1

Fig. 2

31 30 29 28 27 26 25 24 23	0
ZNCVE2E1HL	PC[25:2]

Fig. 3

13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	0		СС			8	bit	sign	ied	offs	et	

Fig. 4

13	12	11	10	9	8_	7	6	5	4	3	2	1	0
0	0	1		a			b				op		

Fig. 5

13	12	11	10	9	8	7	6	5	4	3	2	1	<u> </u>
0	0	1		a		s	ubo	p		0	p=3	1	

Fig. 6

13	12	11	10	9	8	7	6	5	4	3	2	1	0_
0	0	1	In	ıp_	op	su	bop	=7		0	p=3	1	

Fig. 7

13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	1	0		Α			b		op	4	bit	offs	et

Fig. 8

3	12	11	10	9	8	7	6	5	4	3	2	1	0
0	1	1		A			b		op	4	bit	offs	et

Fig. 9

13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	0		Α		op			7 bi	t of	fset	:	1

Fig. 10

<u>13</u>	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	1		Α		ор		7	7 bii	t int	ege	r	

Fig. 11

13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	1	0		Α			op		5	bit	int	ege	r

Fig. 12

	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Ì	1	1	1		a		_		h				op	

Fig. 13

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	0	0		cc			8	bit :	sign	ed	offs	et	

Fig. 14

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	0	1		a			b				op		

Fig. 15

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	0	1		a		SI	ubo	p		0	p=3	1	

Fig. 16

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	0	1	im	ip_e	op	su	bop	=7		0	p=3	1	

Fig. 17

13	12	11	10	9	8	7	6	5	4	3	2	1	0	
0	0	1	0		a			b			5 bi	t of	fset	

Fig. 18

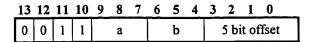


Fig. 19

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0_
0	1	0	0		a		op			7 bi	it of	fset		

Fig. 20

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	1	0	1		a		op		-	7 bii	t int	ege	r	

Fig. 21

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	1	1	0		a			op			5 bit	t int	ege	r

Fig. 22

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	1	1	1		a				h				op	

Fig. 23

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	0	0		a			b		0	op		С	

Fig. 24

14	13	12	11	10	9	8	7	6	5	4	3	2_	1	0
1	0	0	0		a			b		1	op	3	bit i	int

Fig. 25

1	4	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Г		0	0	1		a		op		7	bit	t int	ege	r	

Fig. 26

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	1	0		a			b		op	4	bit	offs	et

Fig. 27

14	13	12	11	10	9	8	7	6	5	4	3	2	1	. 0
1	0	1	1		а	-		b		op	4	bit	offs	et

Fig. 28

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	0	1		a		ор		7	bit	int	ege	r	

Fig. 29

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	0	1		a		ор			7 bi	t int	ege	r	

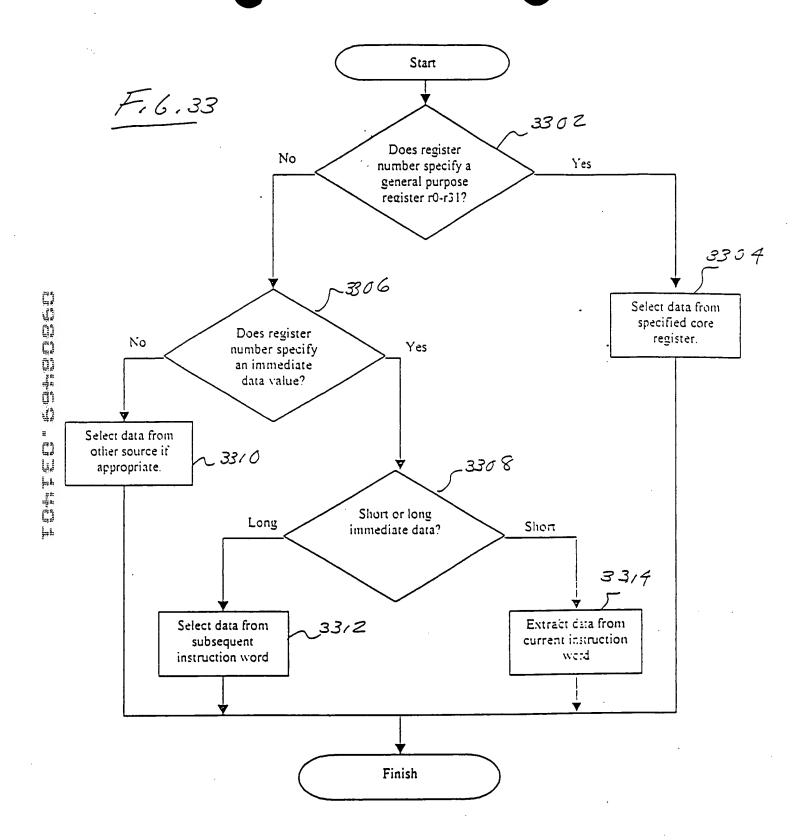
Fig. 30

14	13	12	11	10	9	8	7	6	5	4	3	_2_	1	0
1	1	1	0			1	1 b	it si	igne	d o	ffse	t		

Fig. 31

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	1	1	1					Re	serv	/ed				

Fig. 32



F16. 34

F.6.35

